



APR 29 2004

DOCKET NO. 03DV09036

15W

2832

PATENT APPLICATION
SERIAL NO. 09/682,765

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Alan J. Janicek, et al.

Serial Number: 09/682,765

Filed: October 16, 2001

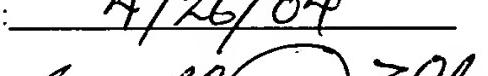
For: QUICK-CONNECT POSITIVE
TEMPERATURE COEFFICIENT OF
RESISTANCE
RESISTOR/OVERLOAD
ASSEMBLY AND METHOD

卷之三

Group Art Unit: 2832

Examiner: Karl Easthom

Mail Stop: Non-Fee Amendment
Commissioner for Patents
P.O Box 1450
Arlington, Virginia 22313-1450

<u>CERTIFICATE OF MAILING</u>
I hereby certify that the paper identified herein is being sent by first class mail, postage prepaid, to Mail Stop: Non-Fee Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:
Date: <u>4/26/04</u>
 <u>Bradley D. Ellis</u>

AMENDMENT

Dear Sir:

In response to the Office Action dated January 2, 2004 (“Office Action”), and the Office Action dated April 5, 2004, Applicant responds as follows:

INTRODUCTORY COMMENTS

The Office Action dated January 2, 2004 has been carefully considered and the following amendments and remarks are presented in a sincere effort to put this application in condition for allowance. By this amendment, Claims 1, 2 and 10 have been amended. Claims 11-21 remain withdrawn without prejudice pursuant to Applicants' October 22, 2002 Response to Restriction Requirement. Claims 1-10 are presently pending.

In response to the Office Action dated April 5, 2004, withdrawn Claims 11-21 are presented.